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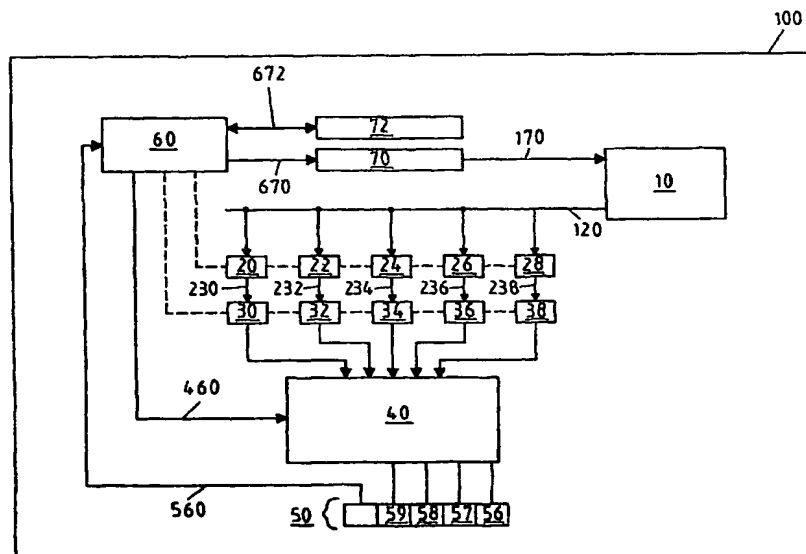
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(54) Title: **DATA PROCESSING DEVICE WITH MICROPROCESSOR AND WITH ADDITIONAL ARITHMETIC UNIT AND
ASSOCIATED METHOD**



(57) Abstract: In order to further develop a data processing device (100; 100') having at least one microprocessor (90) and having at least one additional arithmetic unit (40) as well as a method of performing at least one particular defined calculation by means of the data processing device (100; 100') in such a way that a plurality of calculations may be performed in sequence without any intervention by the microprocessor (90), it is proposed that the registers be loadable from at least one in particular peripheral memory (10), for example from at least one R[andom]A[ccess]M[emory], from at least one R[ead]O[nly]M[emory] or from at least one E[lectrically] E[rasable] P[rogrammable] R[ead]O[nly]M[emory].



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